

**Amendments to the Specification:**

Please replace the paragraph beginning at page 5, line 4, with the following redlined paragraph:

Figure 6 is an electrical schematic of the switches, freewheeling diodes, and ~~decoupling snubber~~ capacitors according to an illustrated embodiment.

Please delete the paragraph beginning at page 5, line 18.

Please replace the paragraph beginning at page 5, line 20, with the following redlined paragraph:

Figures 1, 2A, and 2B show a base power module 10, generally comprising: a lead frame or housing 12, an integrated cold plate 14 attached to the housing 12 via bushings 15, a DC bus 16, an AC bus 18; circuitry 20 electrically coupled between the DC bus 16 and AC bus 18, forming a high side 20a and a low side 20b of the power module 10. The base power module 10 may further include one or more gate drivers 22 (~~Figure 9~~) for driving some of the power semiconductors 20.

Please replace the paragraph beginning at page 5, line 27, with the following redlined paragraph:

Two sets of DC bus terminals 24, 26 extend out of the housing 12. In some applications one set of DC bus terminals ~~24-26~~ is electrically coupled to a positive voltage or high side of a power source or load and the other set of DC bus terminals is ~~26-24~~ is electrically coupled to a negative voltage or low side of the power source or load. In other applications, the DC bus terminals 24, 26 are electrically coupled to respective DC bus terminals 24, 26 on another power module. A set of AC phase terminals comprises three pairs of AC bus phase terminals 28a, 28b, 30a, 30b, 32a, 32b, extending out of the housing 12. In some applications, one pair of AC phase terminals is coupled to a respective phase (A, B, C) of a three phase power source or load. In other applications, some of the AC phase terminals are interconnected across or between the pairs, and coupled to power sources or loads.

Please delete the paragraph at page 10, line 22.

Please delete the paragraph at page 11, line 3.